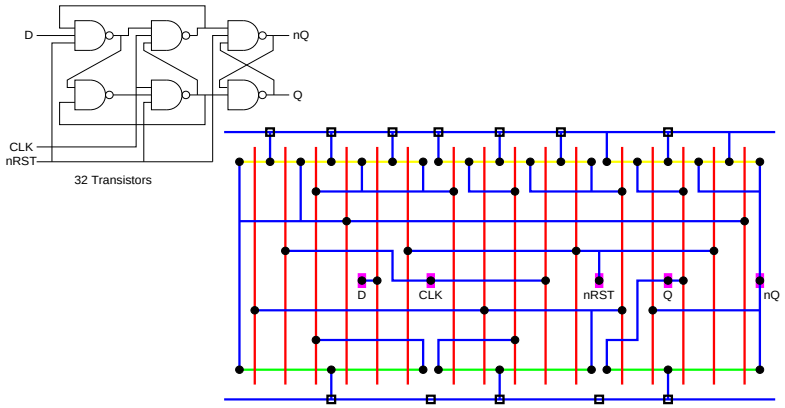
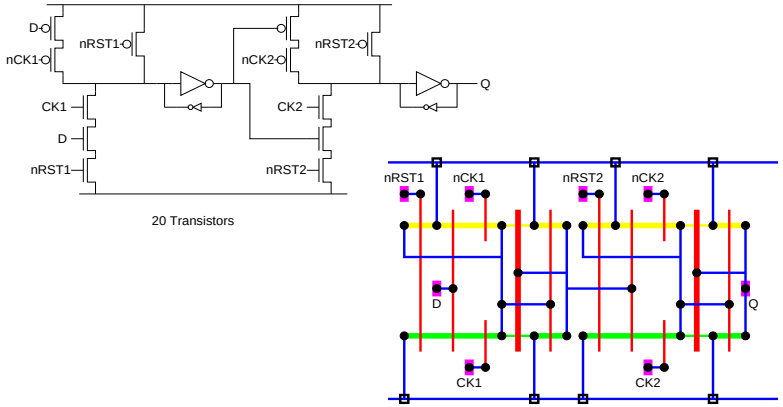
Edge Triggered D-Type with Reset (DFFR)



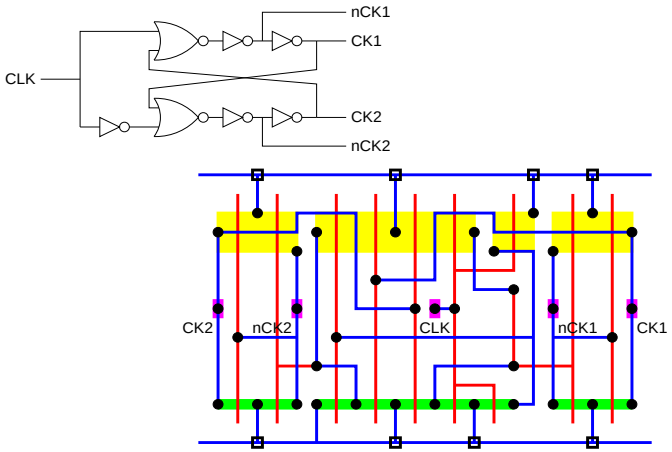
1

Master Slave D-Type with Reset (MSDR)



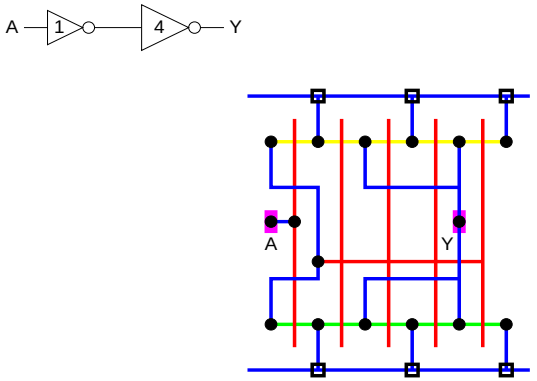
2

Two Phase Clock Generator (CLKGEN)



3

Non-Inverting Buffer (BUF)



4